

CLAIMS

1. A current output type drive circuit for outputting a drive current to a driven object shared by being divided into a plurality of areas,

5 comprising a plurality of drivers arranged corresponding to each the shared area of the driven object,

each driver comprising

an output means for outputting a supplied
10 reference current and the drive current corresponding to image data to a corresponding shared area of the driven object and

a reference current source circuit for sampling and holding the reference current input from a
15 reference current input terminal, then supplying the same to the output means.

2. A current output type drive circuit as set forth in claim 1, wherein said reference current source circuit comprises at least:

20 a current sampling circuit including a current memory for sampling and holding said reference current in accordance with a control signal and

a control circuit for outputting to said current sampling circuit a control signal for controlling
25 write and read operations of said reference current of the current memory of said current sampling circuit.

3. A current output type drive circuit as set forth in claim 2, wherein

said current sampling circuit includes a first current memory and a second current memory, and

5 said control circuit outputs to said current sampling circuit said control signal so as to alternately perform a write operation of the reference current input from said reference current input terminal and a read operation of the written reference current on said first
10 current memory and second current memory.

4. A current output type drive circuit as set forth in claim 2, wherein said output means

includes a plurality of current output type digital/analog conversion circuits and

15 the circuit comprises means for increasing the reference current read from the current memory of the current sampling circuit of said reference current source circuit to a plurality of reference currents by further copying or distributing them by time division, and

20 said plurality of reference currents are supplied to said plurality of digital/analog conversion circuits.

5. A current output type drive circuit as set forth in claim 4, wherein

25 each driver is a driver outputting currents of a plurality of channels in accordance with input data,

further comprises a register array for holding said input data, and

further comprises means for increasing the reference current sampled and held by the reference
5 current source circuit to a plurality of reference currents by further copying or distributing them by time division, and

said output means comprises
a plurality of conversion circuits for
10 receiving said plurality of reference currents and outputting currents in accordance with the data held by the register array and

a current output circuit comprising a first group of current sampling circuits and a second group of
15 current sampling circuits operating alternately in a current write mode and current read mode in accordance with the output currents of the conversion circuits.

6. A current output type drive circuit as set forth in claim 5, wherein

20 said input data is digital image data,
the circuit comprises means for distributing the reference current to the drivers in a vertical blanking period during which operations on said image data are suspended, and

25 each driver uses as the reference current the current held in the reference current source circuit of

the driver after the vertical blanking period in which digital noise is generated along with transfer of said image data.

7. A current output type drive circuit for
5 outputting a drive current to a driven object shared by being divided into a plurality of areas,

comprising a plurality of drivers arranged corresponding to each the shared area of the driven object,

10 each driver comprising

an output means for outputting a supplied reference current as a drive current to the corresponding shared area of the driven object and

a reference current source circuit for
15 sampling and holding a reference current input from a reference current input terminal, then supplying the same to the output means,

the reference current input terminal being connected to a reference current input terminal of
20 another driver by a common current interconnect, and

the reference current being distributed to the reference current source circuits of the drivers by time division.

8. A current output type drive circuit as set
25 forth in claim 7, wherein each driver fetches said reference current from said reference current input

terminal to said reference current source circuit when receiving a signal indicating start of distribution of the reference current and outputs the signal indicating the start of distribution of reference current to the
5 driver circuit of the next stage.

9. A current output type drive circuit as set forth in claim 8, wherein each driver comprises a data memory, writes input data into said data memory when receiving a first signal indicating start of writing of
10 data and outputs said first signal indicating start of writing of data to the driver of the next stage and fetches said reference current from said reference current input terminal to said reference current source circuit in synchronization with said first signal when
15 receiving a second signal indicating start of distribution of reference current and outputs said second signal indicating start of distribution of reference current to the driver circuit of the next stage.

10. A current output type drive circuit as set forth in claim 7, wherein said reference current source circuit comprises at least:

a current sampling circuit including a current memory for sampling and holding said reference current in accordance with a control signal and

25 a control circuit for outputting to said current sampling circuit a control signal for controlling

write and read operations of said reference current of the current memory of said current sampling circuit.

11. A current output type drive circuit as set forth in claim 10, wherein

5 said current sampling circuit includes a first current memory and a second current memory, and
 said control circuit outputs to said current sampling circuit said control signal so as to alternately perform a write operation of the reference current input
10 from said reference current input terminal and a read operation of the written reference current on said first current memory and second current memory.

12. A current output type drive circuit as set forth in claim 10, wherein said output means

15 includes a plurality of current output type digital/analog conversion circuits and

 the circuit comprises means for increasing the reference current read from the current memory of the current sampling circuit of said reference current source
20 circuit to a plurality of reference currents by further copying or distributing them by time division, and

 said plurality of reference currents are supplied to said plurality of digital/analog conversion circuits.

25 13. A current output type drive circuit as set forth in claim 7, wherein at least the reference current

source circuit of the driver serving as a master includes a reference current source circuit generating a reference current and supplying it to said common current interconnect.

5 14. A current output type drive circuit as set forth in claim 10, wherein at least the reference current source circuit of the driver serving as a master includes a reference current source circuit generating a reference current and supplying it to said common current
10 interconnect.

15 15. A current output type drive circuit as set forth in claim 7, wherein

each driver is a driver outputting currents of a plurality of channels in accordance with input data,

15 further comprises a register array for holding said input data, and

 further comprises means for increasing the reference current sampled and held by the reference current source circuit to a plurality of reference
20 currents by further copying or distributing them by time division, and

said output means comprises

 a plurality of conversion circuits for receiving said plurality of reference currents and
25 outputting currents in accordance with the data held by the register array and

a. current output circuit having a first group of current sampling circuits and a second group of current sampling circuits operating alternately in a current write mode and current read mode in accordance with the output currents of the conversion circuits.

16. A current output type drive circuit as set forth in claim 15, wherein

said input data is digital image data,

the circuit comprises means for distributing the reference current to the drivers in a vertical blanking period during which operations on said image data are suspended, and

each driver uses as the reference current the current held in the reference current source circuit of the driver after the vertical blanking period in which digital noise is generated along with transfer of said image data.

17. A current output type drive circuit as set forth in claim 7, wherein the interconnect of said reference current is arranged between power supply interconnects for shield.

18. A current output type drive circuit as set forth in claim 7, wherein the interconnect of said reference current, when a multilayer interconnect including a power supply layer for shield, is arranged at a top layer of said power supply layer for shield.

19. A current output type drive circuit as set forth in claim 7, further comprising means for suppressing great fluctuations of the potential of said common reference current interconnect when the circuits sampling and holding the reference currents of the drivers are all off.

20. A current output type drive circuit as set forth in claim 12, wherein
said means increasing said reference current to a plurality of reference currents comprises a current mirror circuit configured by a constant current source including resistor elements arranged at the input stage and a plurality of reference current sources including resistor elements arranged at the output stage in parallel so as to correspond to the output parts of said output means, and

the resistor elements of the reference current sources arranged at the two ends among said plurality of reference current sources are arranged close to the resistor elements of said constant current source.

21. A current output type drive circuit as set forth in claim 20, wherein the resistor elements forming the reference current sources are laid out divided and cross-laced.

22. A display device for outputting a drive current to a shared area of a display panel shared by

being divided into a plurality of areas,

comprising a plurality of drivers arranged
corresponding to each the shared area of the display
panel,

5 each driver comprising an output means for
outputting a supplied reference current to a
corresponding shared area of the driven object and

a reference current source circuit for
sampling and holding the reference current input from a
10 reference current input terminal, then supplying the same
to the output means.

23. A display device for outputting a drive
current to a shared area of a display panel shared by
being divided into a plurality of areas,

15 comprising a plurality of drivers arranged
corresponding to each the shared area of the display
panel,

each driver comprising

an output means for outputting a supplied
20 reference current to a corresponding shared area of the
driven object and

a reference current source circuit for
sampling and holding a reference current input from a
reference current input terminal, then supplying the same
25 to the output means,

the reference current input terminal being

connected to a reference current input terminal of
another driver by a common current interconnect, and
the reference current being distributed to
the reference current source circuits of the drivers by
5 time division.

24. A display device as set forth in claim 23,
wherein each driver fetches said reference current from
said reference current input terminal to said reference
current source circuit when receiving a signal indicating
10 start of distribution of reference current and outputs
the signal indicating start of distribution of reference
current to the driver circuit of the next stage.

25. A display device as set forth in claim 23,
wherein each driver comprises a data memory, writes input
15 data into said data memory when receiving a first signal
indicating start of writing of data and outputs said
first signal indicating start of writing of data to the
driver of the next stage and fetches said reference
current from said reference current input terminal to
20 said reference current source circuit in synchronization
with said first signal when receiving a second signal
indicating start of distribution of reference current and
outputs said second signal indicating start of
distribution of reference current to the driver circuit
25 of the next stage.

26. A display device as set forth in claim 23,

wherein the interconnect of said reference current is arranged between power supply interconnects for shield.

27. A display device as set forth in claim 23,
wherein the interconnect of said reference current, when
5 a multilayer interconnect including a power supply layer
for shield, is arranged at a top layer of said power
supply layer for shield.

28. A display device as set forth in claim 23,
further comprising means for suppressing great
10 fluctuations of the potential of said common reference
current interconnect when the circuits sampling and
holding the reference currents of the drivers are all off.